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Steven J. Elleman

41,733  
Reg. No.

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicant : Wiegele et al.  
Serial No. : 10/620,119  
Filed : July 15, 2003  
Title : MICRO MIRROR ARRAYS AND MICROSTRUCTURES WITH  
SOLDERABLE CONNECTION SITES  
Docket : 015559-00288  
Examiner : Tina Mei Seng Wong  
Art Unit : 2874

Commissioner For Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Applicants acknowledge the Examiner's Statement of Reasons for Allowance accompanying the Notices of Allowance mailed September 21, 2006 and January 8, 2007, and the following comments are provided for the public record. While Applicants do not disagree with the Examiner's statements that the prior art of record fails to show certain claim limitations, Applicants note that their invention is defined by the claims of the application. Therefore, in accordance with the applicable statutes, regulations and case law, Applicants reserve the right to distinguish their invention over the prior art based upon any element or combination of elements set forth in the claims.

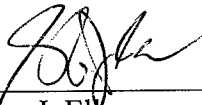
Applicants hereby put the public on notice that the Examiner's stated reasons for allowance are not considered to limit the claims and each of the claims will be interpreted and

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enforced as broadly as it is written. Applicants further note that the entire range of equivalents that may be available for each claim limitation is hereby reserved and is not affected by any of the Examiner's stated reasons for allowance.

Finally, it is noted that in the Examiner's Statement for Reasons for Allowance accompanying the Notice of Allowance mailed September 21, 2006, the phrase "wafer portion" at page 3, lines 1-2, should instead apparently be "coverage area."

Respectfully submitted,

  
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